20th November, 2018

Advertisement

Applications are invited from bright, young candidates for the following posts under the SMDP-C2SD project funded by MEITY Government of India, in Discipline of Electrical Engineering.

Name of Post	:	Lab Engineer
Consolidated Monthly Pay	:	Rs. 34,000/- per month consolidated
Qualification and Experience	:	M. E./M. Tech. in Electrical/Electronics/Computer Science Engineering and related disciplines with a valid GATE score and knowledge in VLSI Design (Analog or Digital) or Microelectronics or equivalent. Working knowledge of VLSI CAD/TCAD tools like Cadence/Synopsis/MentorGraphics/SILVACO is required. Experience in LINUX operating system is desired.
Tenure	:	One year extendable up to three years on satisfactory performance

Eligible candidates may apply through online application form on or before 10th December, 2018. Selection will be based upon interview and shortlisted candidates will be intimated well in advance the date and time of the interview. The interview will be conducted in the 2nd or 3rd week of December.

Link of online form:

https://docs.google.com/forms/d/e/1FAIpQLSebHOVfyewKntNmVzxQk5TCWQ_yklpsqmmX2JxA5vXQFd0F
KA/viewform?usp=sf_link